

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	56	(plural\$5 or multip\$5 or many or two) adj3 processor and (processor or instruction) (selector or switch or multiplex\$3) and (MIPS and ARM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2008/10/28 12:53
L2	40	(plural\$5 or multip\$5 or many or two) adj3 processor and instruction (translat\$4 or interpret\$3 or conver\$4) and (MIPS and ARM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2008/10/28 12:53
L3	31	reconfigur\$5 (process\$4) adj core and (plural \$6 or multip\$5) processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2008/10/28 12:53
L4	84	different near3 processor near3 (architec\$4 or type or famil\$4) and first adj3 processor and second adj3 (processor) and (MIPS or ARM) and (processor or instruction or mode) with (select\$3 or switch\$3 or multiplex\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2008/10/28 12:53

L5	5	different near3 processor near3 (architec\$4 or type or famil\$4) and (processor or instruction or mode) adj (selector or switch or multiplex\$3) and (MIPS and ARM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2008/10/28 12:53
L6	40	(plural\$5 or multip\$5 or many or two) adj3 processor and instruction (translat\$4 or interpret\$3 or conver\$4) and (MIPS and ARM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2008/10/28 12:53
L7	132	dual\$1core processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2008/10/28 12:53
L8	41	first adj3 processor and second adj3 processor and (switch or selector) with processor and translat\$3 and (arm and mips)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2008/10/28 12:53

L9	37	(dual (core or processor) or (first (core or processor) and second processor)) with ((common or single or "same") integrated circuit or SOC or system\$1on \$1chip or system on chip) and ((program \$4 or configur \$6) adj3 processor)	US-PPGPUB; USPAT; USOCCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2008/10/28 12:54
L10	40	(plural\$5 or multip\$5 or many or two) adj3 processor and instruction (translat\$4 or interpret\$3 or conver\$4) and (MIPS and ARM)	US-PPGPUB; USPAT; USOCCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NEAR	ON	2008/10/28 12:54

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